

STANDARDS CHANGES CATALOG (SCC)

SCC NUMBER: SCC 172R1

CHANGE PROPOSAL TITLE: Clarification of F bit usage in SREJ.

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ORIGINATOR'S INTERNAL NUMBER: N/A

AFFECTED DOCUMENTS: This change is for MIL-STD-188-220C.

PRECEDENCE: Routine

RECOMMENDATIONS: None

RECORD OF PROCESSING

<u>DATE</u> :	<u>ACTION</u> :
24 March 2004	Proposal
24 March 2004	Work Item
<u>29 March 2004</u>	<u>Revision 1</u>

1. STATEMENT OF THE PROBLEM:

The standard does not fully address the use of the Final (F) bit in the Type 2 SREJ response.

2. PROBLEM ANALYSIS:

The description of the Type 2 SREJ command and response focuses on the Poll (P) bit and ignores the F bit. The SREJ response needs clarification.

3. PROPOSED SOLUTION:

- a. Modify paragraph 5.3.6.2.2 to add a reference paragraph. (See Encl 1).
- b. Modify paragraph 5.3.6.2.2.4 to include the F bit. (See Encl 2)
- c. Correct paragraph 5.3.7.3.6.4.2 to replace the P bit with the F bit in the SREJ response. (See Encl 3)
- d. Make appropriate changes to Appendix B. (See Encl 4, page 139)

4. ALTERNATIVE SOLUTIONS: None.

5. SYSTEM CHANGES REQUIRED:

6. CONFIGURATION ITEM DOCUMENTATION CHANGES: MIL-STD-188-220C.

7. IMPACT ON INTEROPERABILITY: None.

8. IMPACT ON RELATED DOCUMENTS: None.

9. IMPLEMENTATION DATES: First quarter of CY 2005 Upon approval.

10. OTHER CONSIDERATIONS: None.

11. REFERENCES: None.

12. TROUBLE REPORTS (TRs) ADDRESSED IN THIS SCC: None.